

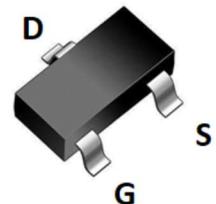
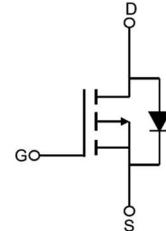


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MAITUO ELECTRONIC

## MSP7016 P-channel Enhancement Mode Power MOSFET

### Features

- -16V, -7A
- $R_{DS(ON)} < 24.5\text{m}\Omega$  @  $V_{GS} = -4.5\text{V}$
- $R_{DS(ON)} < 32\text{m}\Omega$  @  $V_{GS} = -2.5\text{V}$
- Advanced Trench Technology
- Provide Excellent  $R_{DS(ON)}$  and Low Gate Charge
- Lead free product is acquired



### Application

- Load Switch
- PWM Application
- Power management

Marking : P72

SOT 23-3L

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter		Max.	Units
$V_{DSS}$	Drain-Source Voltage		-16	V
$V_{GSS}$	Gate-Source Voltage		$\pm 12$	V
$I_D$	Continuous Drain Current	$T_A = 25^\circ\text{C}$	-7	A
		$T_A = 100^\circ\text{C}$	-4.6	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>		-28	A
$P_D$	Power Dissipation	$T_A = 25^\circ\text{C}$	2	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient		62.5	$^\circ\text{C}/\text{W}$
$T_J, T_{STG}$	Operating and Storage Temperature Range		-55 to +150	$^\circ\text{C}$



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## Electrical Characteristics ( $T_J=25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D = -250\mu\text{A}$	-16	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = -20\text{V}$ , $V_{GS} = 0\text{V}$ ,	-	-	-1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 12\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = -250\mu\text{A}$	-0.4	-0.7	-1.0	V
$R_{DS(\text{on})}$ note2	Static Drain-Source on-Resistance	$V_{GS} = -4.5\text{V}$ , $I_D = -7\text{A}$	-	18.7	24.5	$\text{m}\Omega$
		$V_{GS} = -2.5\text{V}$ , $I_D = -5\text{A}$	-	22.7	32	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = -10\text{V}$ , $V_{GS} = 0\text{V}$ , $f = 1.0\text{MHz}$	-	2000	-	pF
$C_{oss}$	Output Capacitance		-	242	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	231	-	pF
$Q_g$	Total Gate Charge	$V_{DS} = -10\text{V}$ , $I_D = -3\text{A}$ , $V_{GS} = -4.5\text{V}$	-	15.3	-	nC
$Q_{gs}$	Gate-Source Charge		-	2.2	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	4.4	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -10\text{V}$ , $I_D = -7\text{A}$ , $V_{GS} = -4.5\text{V}$ , $R_{\text{GEN}} = 2.5\Omega$	-	10	-	ns
$t_r$	Turn-on Rise Time		-	31	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	28	-	ns
$t_f$	Turn-off Fall Time		-	8	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	-7	A	
$I_{SM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-28	A	
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS} = 0\text{V}$ , $I_s = -7\text{A}$	-	-0.8	-1.2	V

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

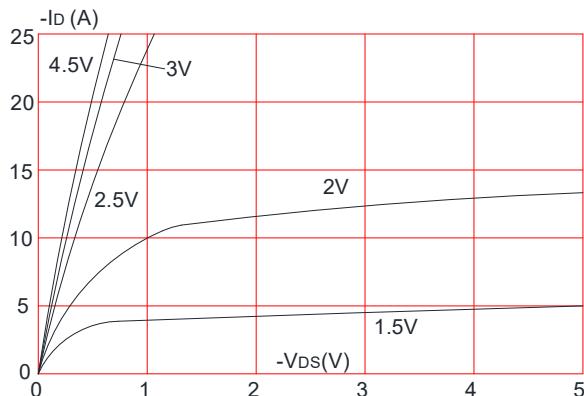
2. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 2\%$



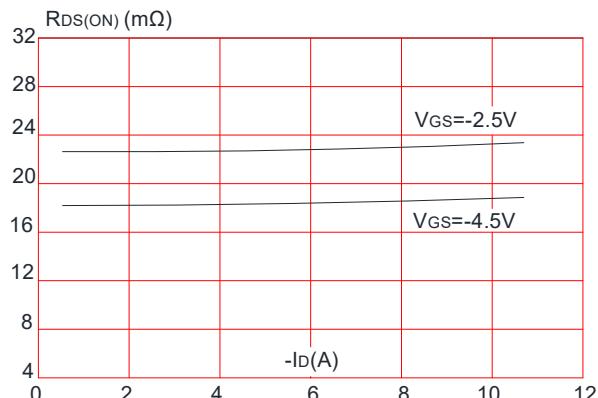
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## Typical Performance Characteristics

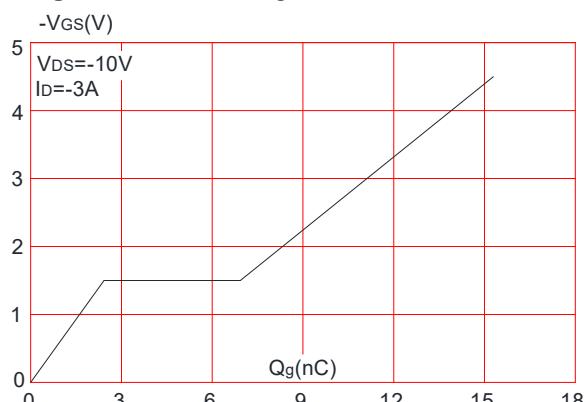
**Figure 1:** Output Characteristics



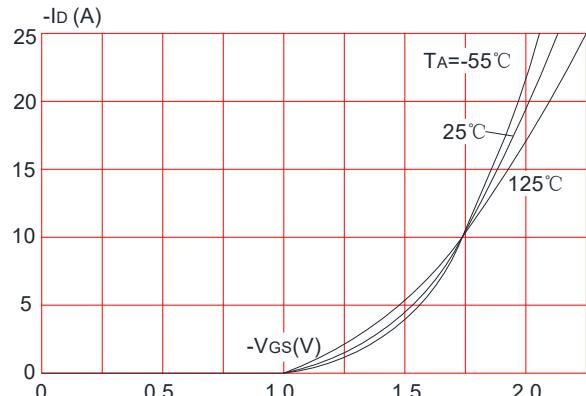
**Figure 3:** On-resistance vs. Drain Current



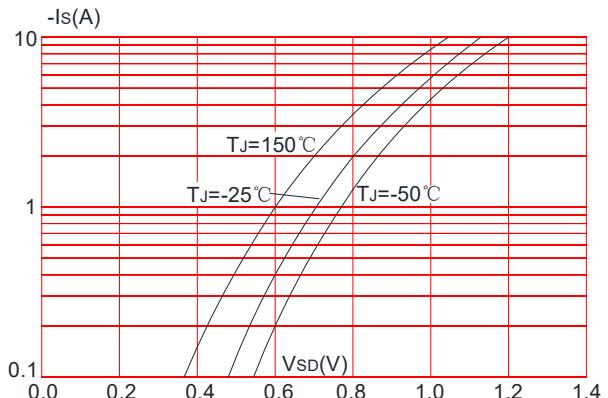
**Figure 5:** Gate Charge Characteristics



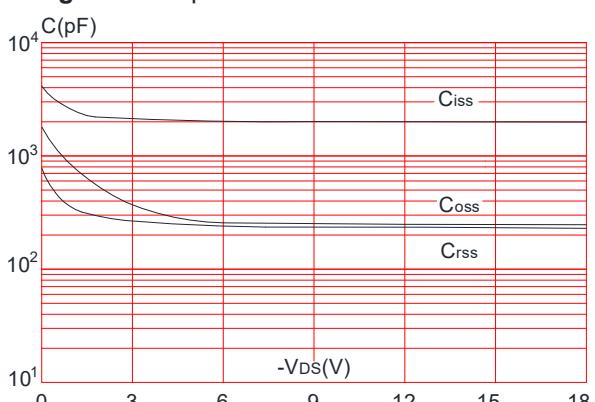
**Figure 2:** Typical Transfer Characteristics



**Figure 4:** Body Diode Characteristics



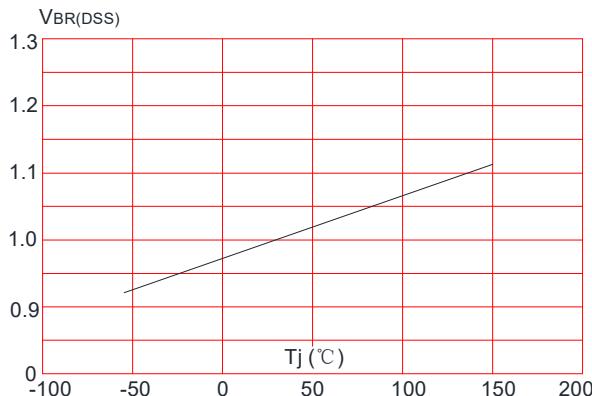
**Figure 6:** Capacitance Characteristics



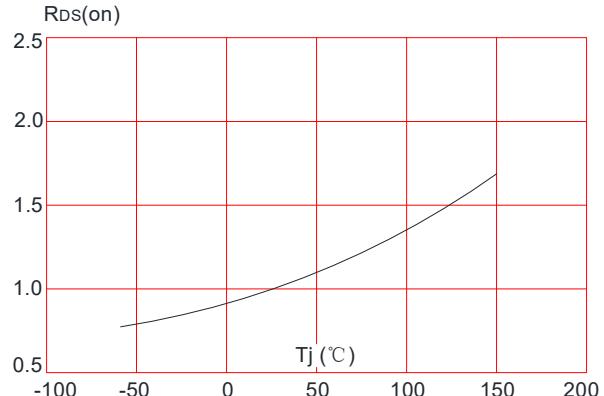


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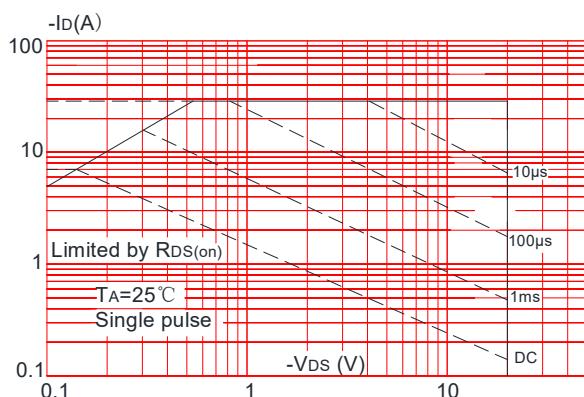
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



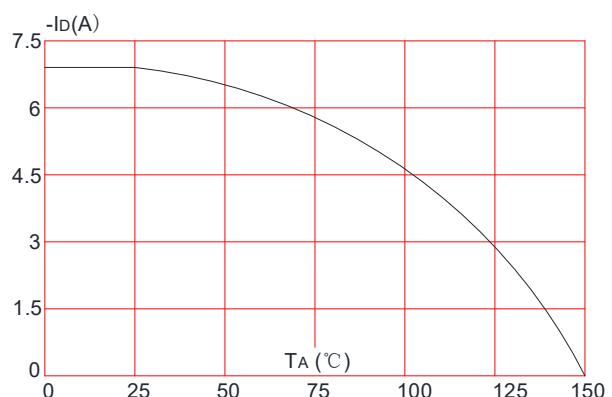
**Figure 8:** Normalized on Resistance vs. Junction Temperature



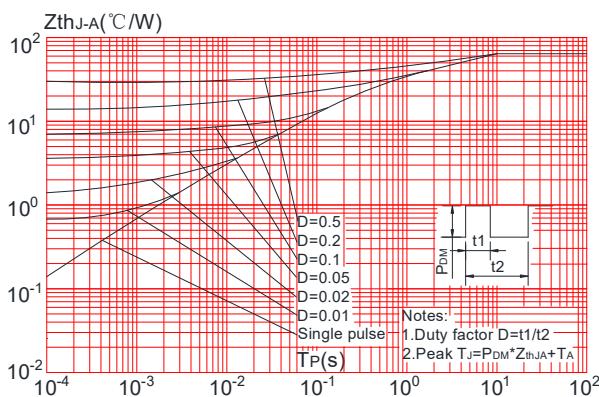
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Ambient

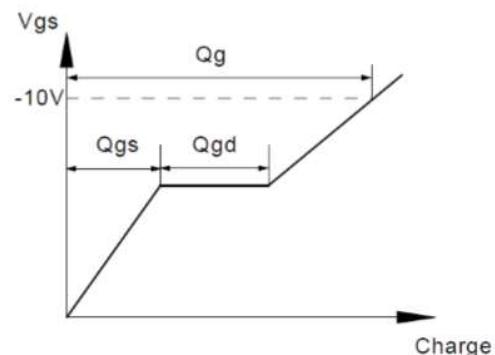
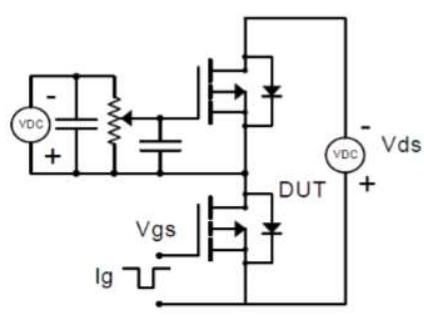




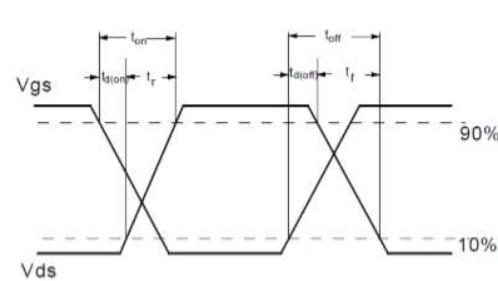
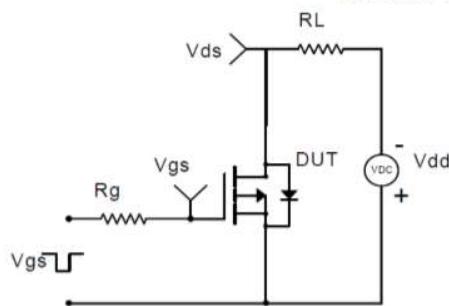
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## Test Circuit

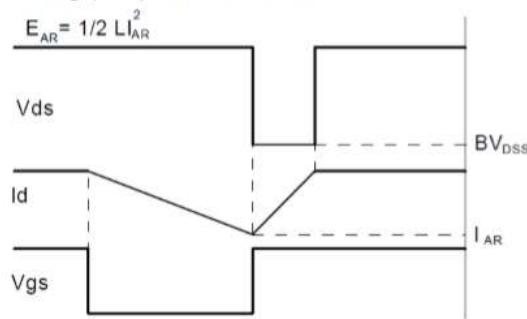
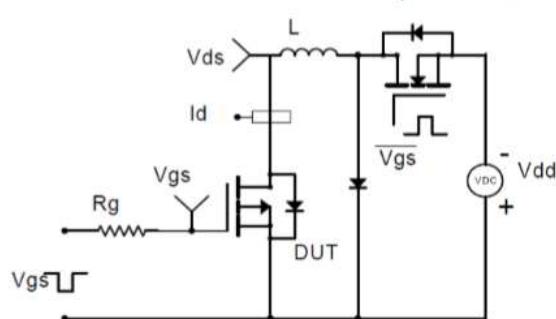
Gate Charge Test Circuit & Waveform



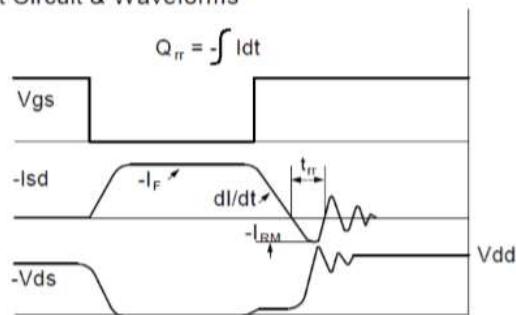
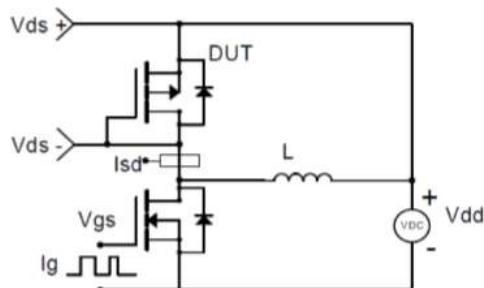
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



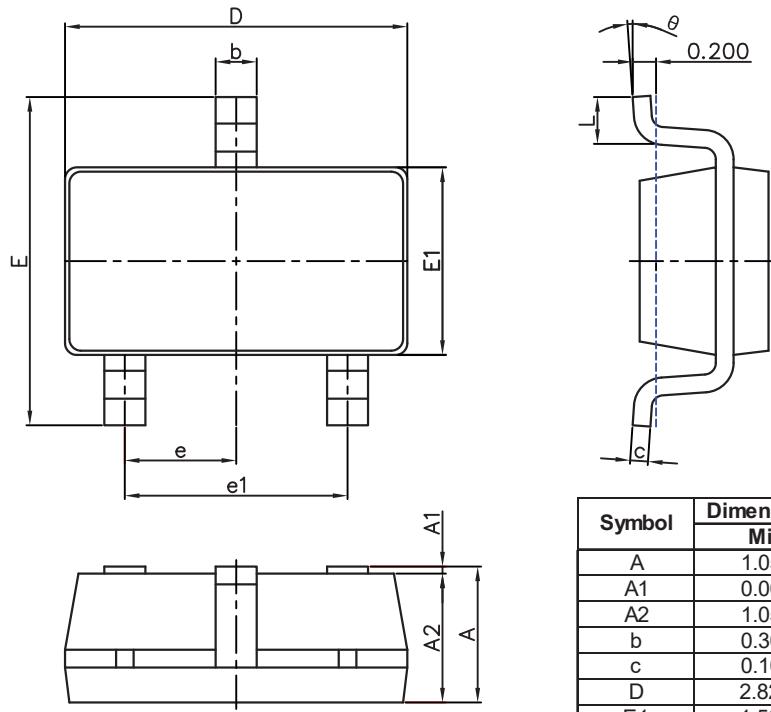
Diode Recovery Test Circuit & Waveforms





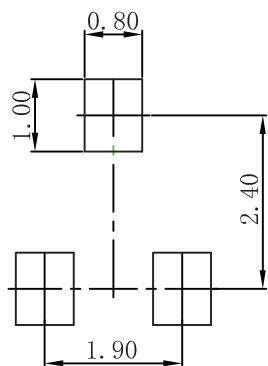
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### SOT23-3L Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E1	1.500	1.700	0.059	0.067
E	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
K	0°	8°	0°	8°

### SOT23-3L Suggested Pad Lay out



#### Note:

1. Controlling dimension:in millimeters.
- 2.General tolerance: $\pm 0.05\text{mm}$ .
- 3.The pad layout is for reference purposes only.